

EL 979950242

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 09/848,846
Filing Date May 3, 2001
Inventor Luan C. Tran
Assignee Micron Technology, Inc.
Group Art Unit 2813
Examiner Laura M. Schillinger
Attorney's Docket No MI22-1689
Title: Semiconductor Processing Methods of Forming Integrated Circuitry

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References -- See Attached Form PTO-1449

The attached form PTO-1449 is submitted in compliance with 37 CFR §1.56. No admission is made regarding whether the submitted references are prior art.

Citation of these references is respectfully requested.

Respectfully submitted,

Dated: Dec. 19, 2003

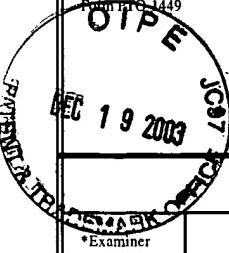
Attorney:


Deepak Malhotra
Reg. No. 33,560

RECEIVED
DEC 30 2003
TECHNOLOGY CENTER 2800

EL 979950242

Sheet 1 of 1

 LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)		U.S. DEPARTMENT OF COMMERCE		ATTY. DOCKET NO.	SERIAL NO.		
		PATENT AND TRADEMARK OFFICE		MI22-1689	09/848,846		
				APPLICANT	LUAN C. TRAN		
				FILING DATE	May 3, 2001	GROUP	2813
U.S. PATENT DOCUMENTS							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA	6,580,149 B2	06/17/03	Tran et al.	257	549	
	AB	6,423,620 B2	07/23/02	Pan et al.	438	597	
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
FOREIGN PATENT DOCUMENTS							
		Document Number	Date	Country	Class	Subclass	Translation
	AL						
	AM						
	AN						
	AO						
	AP						
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)							
	AR						
	AS						
	AT						
EXAMINER				DATE CONSIDERED			
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							